Q1: GitHub

<https://github.com/Carciax/EEE3088F-Project-CKR>

Q2: Power Subsystem Failure Management

Q3: Sensing Subsystem Failure Management

Q4: Microcontroller Subsystem Failure Management

For individual component failure, components will be connected into the circuit by some means that it can be disconnected. Components that are likely to fail will get jumpers which can be easily remove and another component can be connected from an external board.

Components that are less likely to fail will get two pads soldered together in line with the component. These can easily be soldered and de-soldered, and the signal can be measured in that line. Lastly, components that are likely never going to fail will get 0Ω resistors which can be taken out and/or rerouted to other lines.

**Power and controls:**

The power and ground of the microcontroller has solder pads in place to ensure that the microcontroller can function without the power and sensors. The debugger input is already a pin header and thus can be plugged – which also means that the NSRT reset pin has a pin header that can be easily accessed to reset the microcontroller. There are enough redundant push buttons that failure management is unnecessary. The BOOT0 or boot mode pin has a solder pad in case the board needs to be booted in a different mode.

**Sensors and EEPROM:**

The digital sensors inputs and the EEPROM communication lines have 0Ω resistor pairs in place such that they can change between I2C1 and I2C2 communication protocol, and so that there are a pair of lines available for isolated testing of components. The analogue sensors have 0Ω resistor pairs such that they can change between GPIOA and GPIOB reading the value.

**Output:**

The output USB has a 3 pin header on both Tx and Rx lines such that the USB can be changed between UART1 and UART2 as well as the board can communicate with an external USB setup in case the onboard USB port or differential pair lines to UART chip are damaged. The LED has also been wired with a jumper such that an output signal can be jumped to an external board.

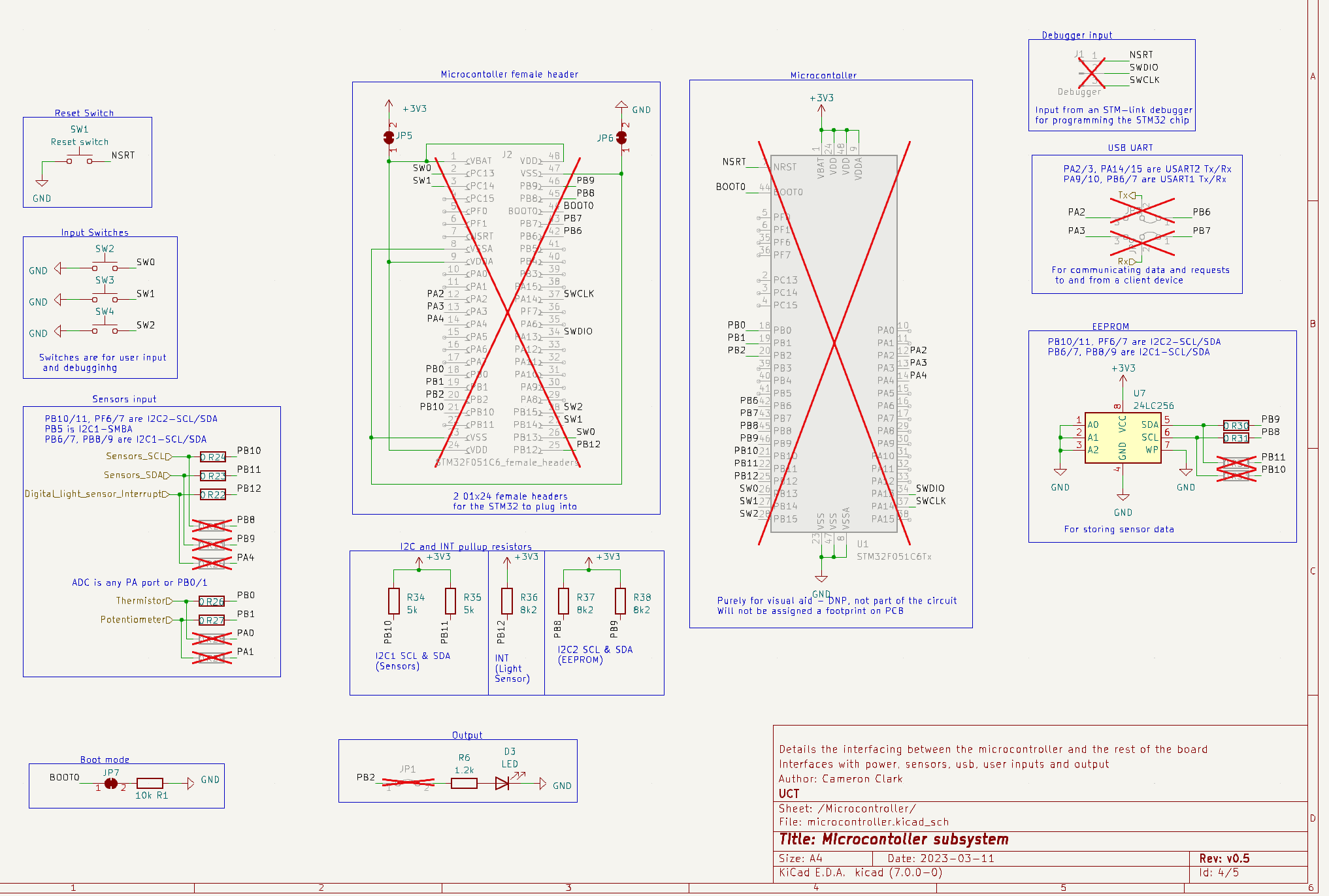
This system of failure management allows for most components to be isolated for testing, redirected in case of line damage, or have signals inputted or outputted to an external board for testing.

Q5: Power Subsystem Schematic

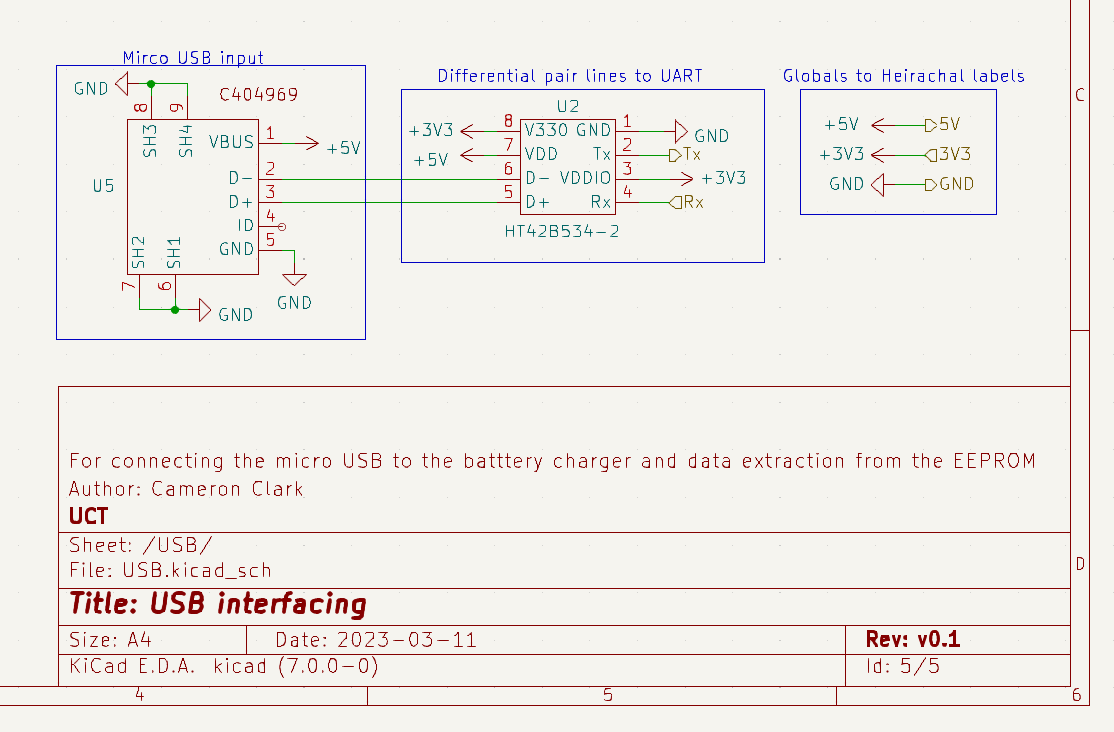
Q6: Sensing Subsystem Schematic

Q7: Microcontroller Subsystem Schematic

The microcontroller schematic



The USB input schematic



Q8: Planned ERCs

Q9: Updated BOM